

REMARKS

Applicant thanks the examiner for pointing out the allowable subject matter and respectfully requests reconsideration of this application and consideration of the following remarks.

Claims 7-10, 13, 14, 17, 18, 20-24 and 31-34 were objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Claims 1-4, 11-12, 15-16, 19 and 25-28 were rejected under 35 U.S.C. 102(e) as being anticipated by Karjalainen (U.S. Patent No. 6,392,660, referred to as Karjalainen). Claims 5-6 and 29-30 were rejected under 35 U.S.C. 103(a) as being unpatentable over Karjalainen in view of Ranganathan (U.S. Patent No. 6,101,620, referred to as Ranganathan).

Claims 7-10, 13, 14, 17, 18, 20-24 and 31-34 are amended; claims 1-6, 11-12, 15-16, 19 and 25-30 are cancelled; new claims 35-49 are added; thus, claims 7-10, 13, 14, 17, 18, 20-24 and 31-49 are pending.

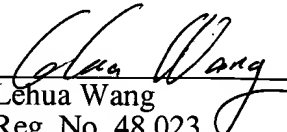
Applicant respectfully submits that the pending claims 7-10, 13, 14, 17, 18, 20-24 and 31-49 contain allowable subject matter, as indicated by the examiner in the Office Action, and are in condition for allowance.

Please charge any shortages or credit any overages to Deposit Account No. 02-2666.

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

1. (cancelled) A method of simulating a second font utilizing a first font, comprising:
stripping a first portion from the first font.
2. (cancelled) The method of claim 1 further comprising:
stripping a second portion from the first font.
3. (cancelled) The method of claim 2 further comprising:
displaying the second font.
4. (cancelled) The method of claim 3 further comprising:
receiving an access request for the second font.
5. (cancelled) The method of claim 4 wherein:
the first font comprises an 8×16 font.
6. (cancelled) The method of claim 5 wherein:
the second font comprises an 8×14 font.
7. (amended) A method of simulating a second font utilizing a first font, the method comprising [The method of claim 6 wherein]:
stripping [the first portion comprises] a top line [of the 8×16 font] and a bottom line
from the first font to simulate the second font; [and
the second portion comprises a bottom line of the 8×16 font]
wherein the first font comprises an n×(m+2) font and the second font comprises an
n×m font.

8. (amended) The method of claim [4] 7 wherein:
the first font comprises a 9×16 font; and
the second font comprises a 9×14 font[;
the first portion comprises a top line of the 9×16 font; and
the second portion comprises a bottom line of the 9×16 font].
9. (amended) The method of claim [8 further comprising] 7 wherein the first font
comprises a 8×16 font; and the second font comprises a 8×14 font[:
copying the 9×16 font into memory].
10. (amended) The method of claim 7 further comprising:
copying the [8×16] n×(m+2) font from BIOS into memory.
11. (cancelled) A machine-readable medium containing a plurality of executable
instructions, which when executed on a processor cause said processor to perform a
method of emulating a second font utilizing a first font, the method comprising:
stripping a first portion from the first font; and
stripping a second portion from the first font.
12. (cancelled) The machine-readable medium of claim 11 wherein the method further
comprises:
copying the first font into memory;
receiving an access request for the second font; and
displaying the second font.

13. (amended) A machine-readable medium containing a plurality of executable instructions, which when executed on a processor cause said processor to perform a method of emulating a second font utilizing a first font, the method comprising [The machine-readable medium of claim 12 wherein]:
[the first font comprises a 9×16 font;
the second font comprises a 9×14 font;
the first portion comprises] stripping a top line [of the 9×16 font] and a bottom line from the first font to emulate the second font; [and
the second portion comprises a bottom line of the 9×16 font]
wherein the first font comprises an $n \times (m+2)$ font and the second font comprises an $n \times m$ font.
14. (amended) The machine-readable medium of claim [12] 13 wherein $m = 14$:
the first font comprises a 8×16 font;
the second font comprises a 8×14 font;
the first portion comprises a top line of the 8×16 font; and
the second portion comprises a bottom line of the 8×16 font].
15. (cancelled) An apparatus for emulating a second font utilizing a first font, comprising:
means for stripping a first portion from the first font; and
means for stripping a second portion from the first font.
16. (cancelled) The apparatus of claim 15 further comprising:
means for copying the first font into memory;
means for receiving an access request for the second font; and
means for displaying the second font.

17. (amended) An apparatus for emulating a second font utilizing a first font, comprising
[The apparatus of claim 16 wherein]:
[the first font comprises a 9×16 font;
the second font comprises a 9×14 font;
the first portion comprises] stripping a top line [of the 9×16 font] and a bottom line
from the first font to emulate the second font; [and
the second portion comprises a bottom line of the 9×16 font]
wherein the first font comprises an $n \times (m+2)$ font and the second font comprises an
 $n \times m$ font.
18. (amended) The apparatus of claim [16] 17 wherein $m = 14$ [:
the first font comprises a 8×16 font;
the second font comprises a 8×14 font;
the first portion comprises a top line of the 8×16 font; and
the second portion comprises a bottom line of the 8×16 font].
19. (cancelled) A system comprising:
a processor;
a first memory coupled to the processor; and
the processor emulating a second font by stripping a first portion from a first font and
stripping a second portion from the first font.
20. (amended) A system, comprising [The system of claim 19 further comprising]:
a BIOS memory, the BIOS memory storing a first font and instructions [coupled to
the processor]; and

a processor coupled to the BIOS memory, the processor emulating [the] a second font utilizing the first font in response to the instructions [contained within the BIOS memory].

21. (amended) The system of claim 20 wherein:
the processor [further] emulating the second font by stripping a portion from the first font in response to receiving an access request for the second font [and displaying the second font].
22. (amended) The system of claim [21 wherein] 20 further comprising:
a first memory coupled to the processor, the processor [further emulating the second font by] copying the first font from the BIOS memory into the first memory to emulate the second font.
23. (amended) The system of claim 21 wherein [apparatus of claim 22 wherein]:
the [first font comprises a 9×16 font;
the second font comprises a 9×14 font;
the first] portion comprises a top line and a bottom line of [the 9×16 font; and
the second portion comprises a bottom line of the 9×16] an n×(m+2) font.
24. (amended) The [apparatus] system of claim [22] 23 wherein:
the [first font comprises a 8×16 font;
the] second font comprises [a 8×14] an n×m font[;
the first portion comprises a top line of the 8×16 font; and
the second portion comprises a bottom line of the 8×16 font].

25. (cancelled) A method of simulating a second font comprising a set of characters utilizing a first font comprising a set of characters, the method comprising:
stripping a first portion from a first character of the first font.
26. (cancelled) The method of claim 25 further comprising:
stripping a second portion from the first character of the first font.
27. (cancelled) The method of claim 26 further comprising:
displaying the first character of the second font, the first character of the second font
resulting from the stripping of the first portion and the stripping of the second
portion.
28. (cancelled) The method of claim 27 further comprising:
receiving an access request for the first character of the second font.
29. (cancelled) The method of claim 28 wherein:
the first font comprises an 8×16 font.
30. (cancelled) The method of claim 29 wherein:
the second font comprises an 8×14 font.
31. (amended) A method of simulating a second font comprising a set of characters
utilizing a first font comprising a set of characters, the method comprising [The
method of claim 30 wherein]:
stripping [the first portion comprises] a top line of each character of the [8×16] first
font; and

stripping [the second portion comprises] a bottom line of each character of the [8×16]

first font;

wherein the first font comprises an $n \times (m+2)$ font and the second font comprises an

$n \times m$ font.

32. (amended) The method of claim [28] 31 wherein $m = 14$ [:
the first font comprises a 9×16 font;
the second font comprises a 9×14 font;
the first portion comprises a top line of each character of the 9×16 font; and
the second portion comprises a bottom line of each character of the 9×16 font].
33. (amended) The method of claim 32 [further comprising] wherein n is one of: 8 and 9[:
copying the 9×16 font into memory].
34. (amended) The method of claim 31 further comprising:
copying the [8×16] first font from BIOS into memory.